

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kenji Araki et al.

Atty. Docket No. 075834.00084

Serial No.: 09/ 872,955

Group Art Unit: 2825

Filed: June 2, 2000

Examiner: Helen Rossoshek

Invention: SYSTEM FOR CHECKING WIRING CONFIGURATION
PRINTED CIRCUIT BOARD

AMENDMENT A

Assistant Commissioner of Patents
Washington, D.C. 20231

S I R:

PETITION FOR EXTENSION OF TIME

Applicants hereby petition for a three-month extension of time to respond to the outstanding Office Action under 37 C.F.R. §1.136(a). The time to respond is thus extended to April 18, 2003. Applicants have included a check in the amount of \$930.00 as payment of the required fee set forth in 37 C.F.R. §1.17(a).

In response to the Office Action dated October 18, 2002, please amend the application as follows:

IN THE CLAIMS:

1. (Currently Amended) A printed circuit board wiring configuration [check] analysis system for [checking] analyzing a wiring configuration [which is tentatively designed on a printed circuit board], comprising:

an object determination unit for determining if there exists a high speed signal wiring [as a result of evaluation using a predefined equation which contains at least a part of circuit information of said printed circuit board as a variable], said determination being [executed] made after [extracting] identifying a pair of a driver and a receiver [sequentially from a group